

SRI VENKATESWARA COLLEGE OF ENGINEERING

(AUTONOMOUS)

Karakambadi Road, Tirupati-517507

EXAMINATION BRANCH

M.Tech II Semester (R20) Supplementary Examinations January-2023

TIME TABLE

Time: **AN: 02.00 P.M TO 05.00 P.M**

Date/Day	Power Systems (PS)	Advanced Manufacturing Systems (AMS)	VLSI Design	Computer Science and Engineering (CSE)
03-01-2023 (Tuesday)	Power System Stability & Control (EE20DPC201)	Precision Engineering (ME20DPC201)	CMOS Mixed Signal Design (EC20DPC201)	Advanced Algorithms (CS20DPC201)
05-01-2023 (Thursday)	Restructured Power Systems (EE20DPC202)	Advanced Production and Operations Management (ME20DPC202)	Physical Design Automation (EC20DPC202)	Advanced Computer Architecture (CS20DPC202)
07-01-2023 (Saturday)	Reactive Power Compensation & Management (EE20DPE203)	Advanced Materials Processing (ME20DPE206)	MEMS System Design (EC20DPE205)	Network security (CS20DPE203)
09-01-2023 (Monday)	Smart Grid Technologies (EE20DPE208)	Flexible Manufacturing Systems (ME20DPE209)	Low power VLSI Design (EC20DPE206)	Human Computer Interaction (CS20DPE206)

Note: Any discrepancy in this time table may be brought to the notice of the under signed immediately.



CONTROLLER OF EXAMINATIONS

Date: 26-12-2022



PRINCIPAL